

7/2	228	Subclass
CLASSIFICATION		

PATENT NUMBER

6826681

682668

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

NOV 30 2004

SCANNED

9

APPLICATION NO. 09/882285	CONT/PRIOR	CLASS 7.11	SUBCLASS	ART UNIT 2185	EXAMINER <u>Kim</u>
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Kevin Kissell
Hartvig Ekner

TITLE: Y D APPLICANTS

~~Method and apparatus for saving and restoring processor registers
values and allocating and deallocating stack memory~~

Certificate

PTO-204
12/00

AUG 22 2006

of Correction

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
712	275		712	229					
INTERNATIONAL CLASSIFICATION			718	108					
G06F	7	40							

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 22	Figs. Drwg. 22	Print Fig. 4	Total Claims 80	Print Claim for O.G. 1
<p><input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.</p> <p><input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____</p>	<p>_____ (Assistant Examiner) (Date)</p>			NOTICE OF ALLOWANCE MAILED	
	<p><i>(Signature)</i> KENNETH S. KIM PRIMARY EXAMINER 7-15-04</p> <p>_____ (Primary Examiner) (Date)</p>			ISSUE FEE	
				Amount Due 41330	Date Paid 10 22 04 JSM
<p><input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.</p>	<p><i>(Signature)</i> (Legal Instruments Examiner) 7/22/04 (Date)</p>			ISSUE BATCH NUMBER	
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